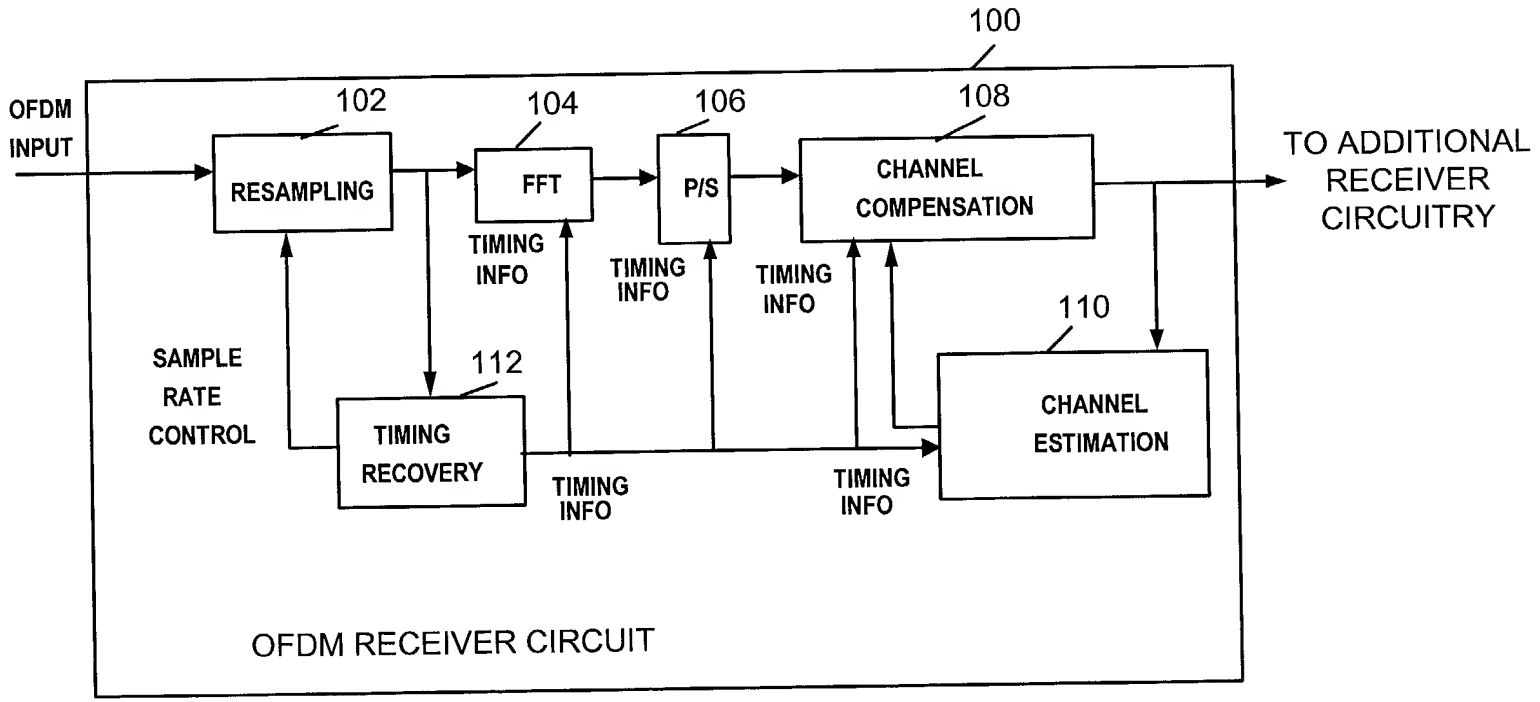
100
OFDM INPUT
102
RESAMPLING
104
FFT
106
P/S
108
CHANNEL COMPENSATION
TO ADDITIONAL RECEIVER CIRCUITRY
TIMING INFO
TIMING INFO
TIMING INFO
110
SAMPLE RATE CONTROL
112
TIMING RECOVERY
CHANNEL ESTIMATION
TIMING INFO
TIMING INFO
OFDM RECEIVER CIRCUIT

Figure 1

09/965,594 - 09/25/2001

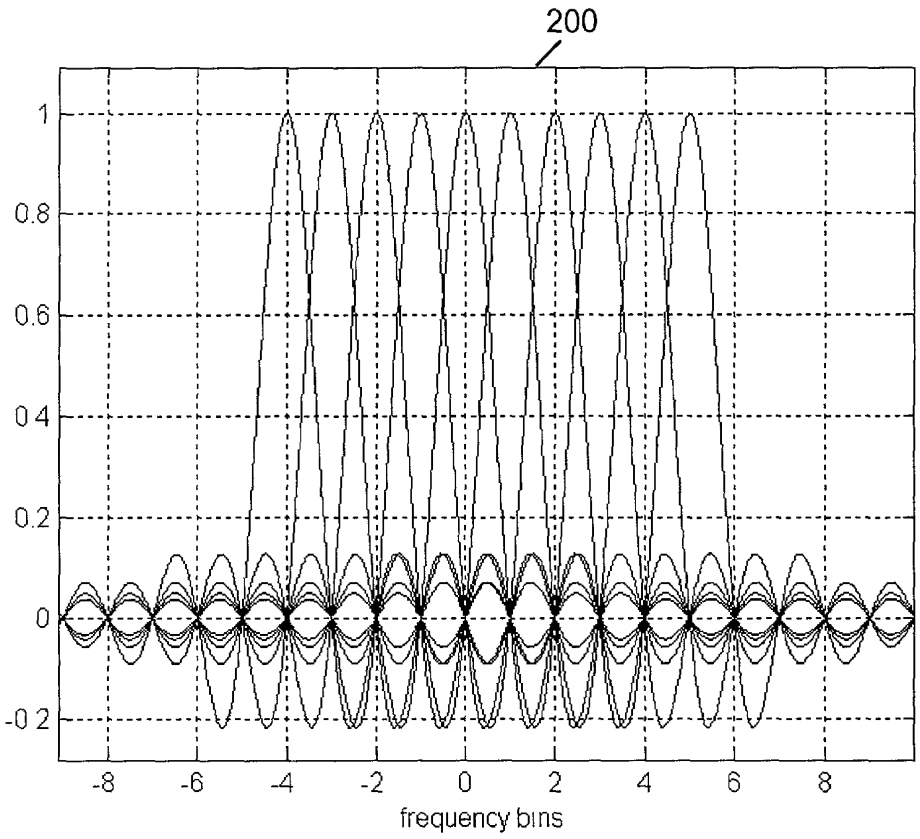

Figure 2

Docket No. 95-1-0054

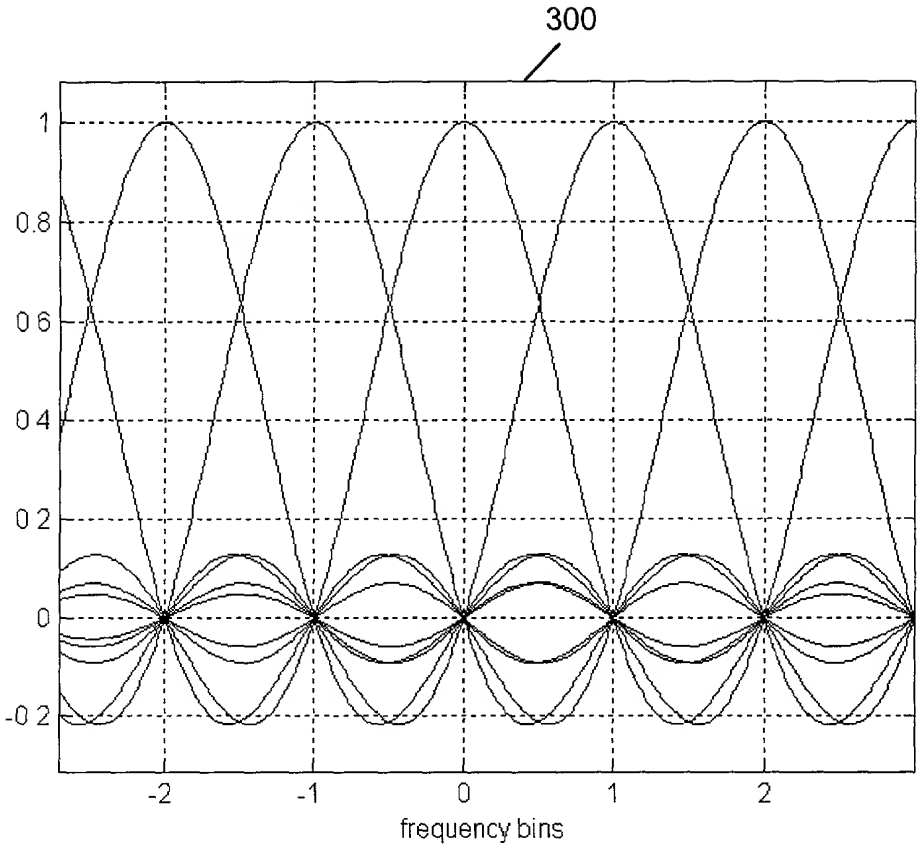

Figure 3

Telefonica - Exhibit 1005 - Page 5

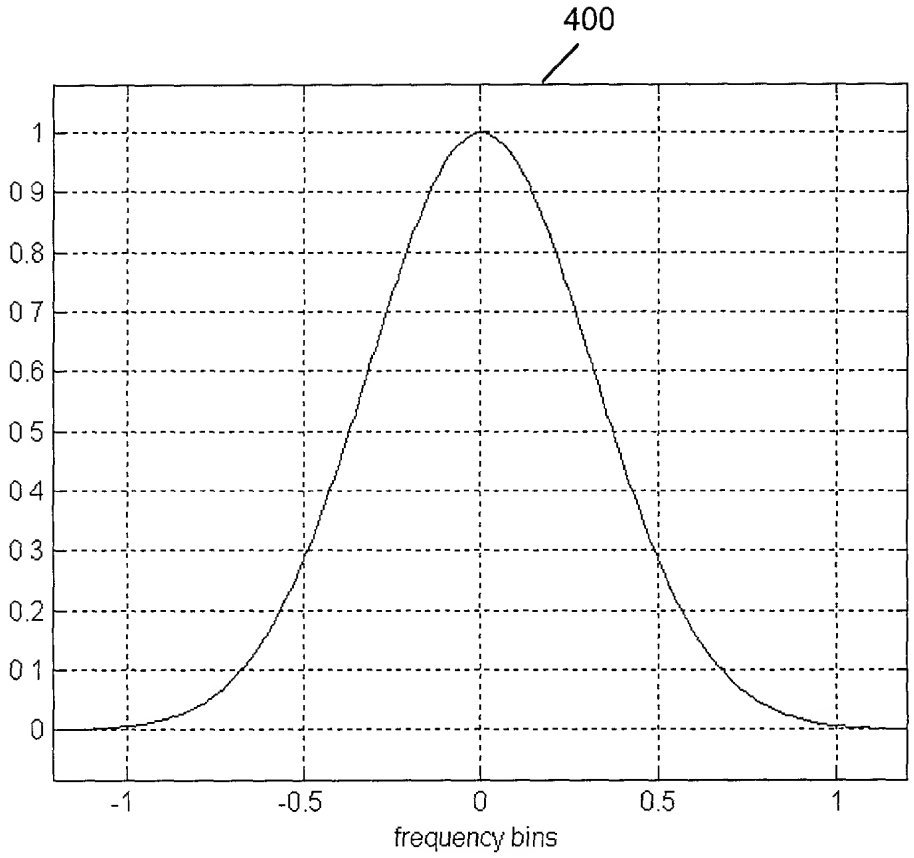

Figure 4

Doc 9674 - 05/25/04

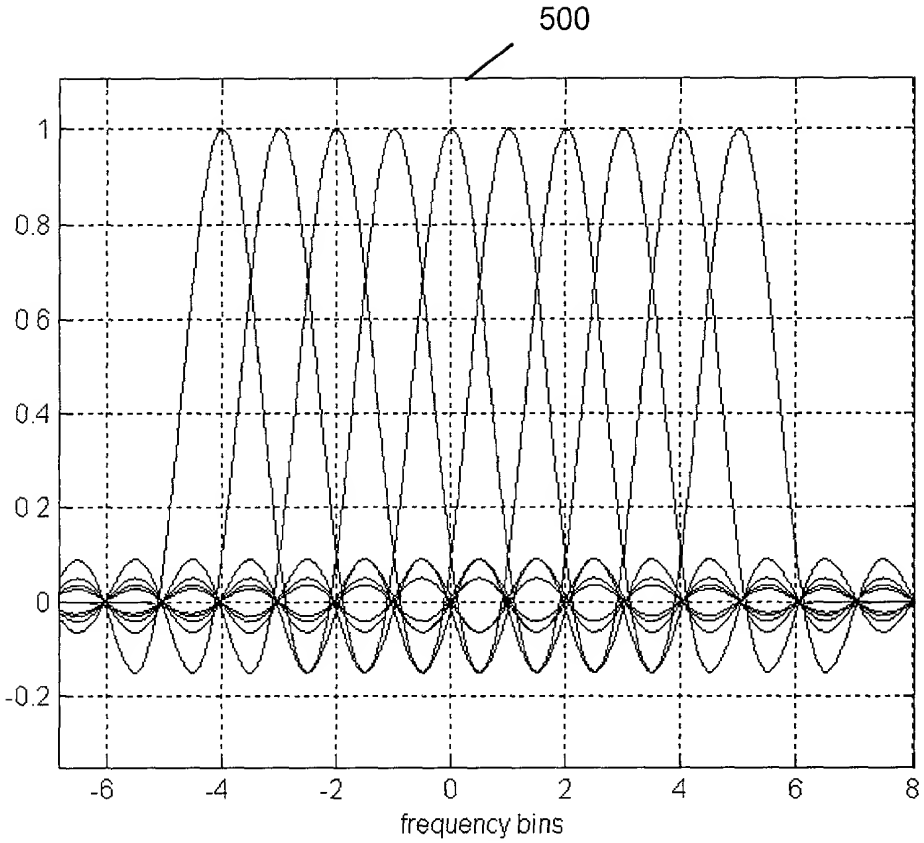

Figure 5

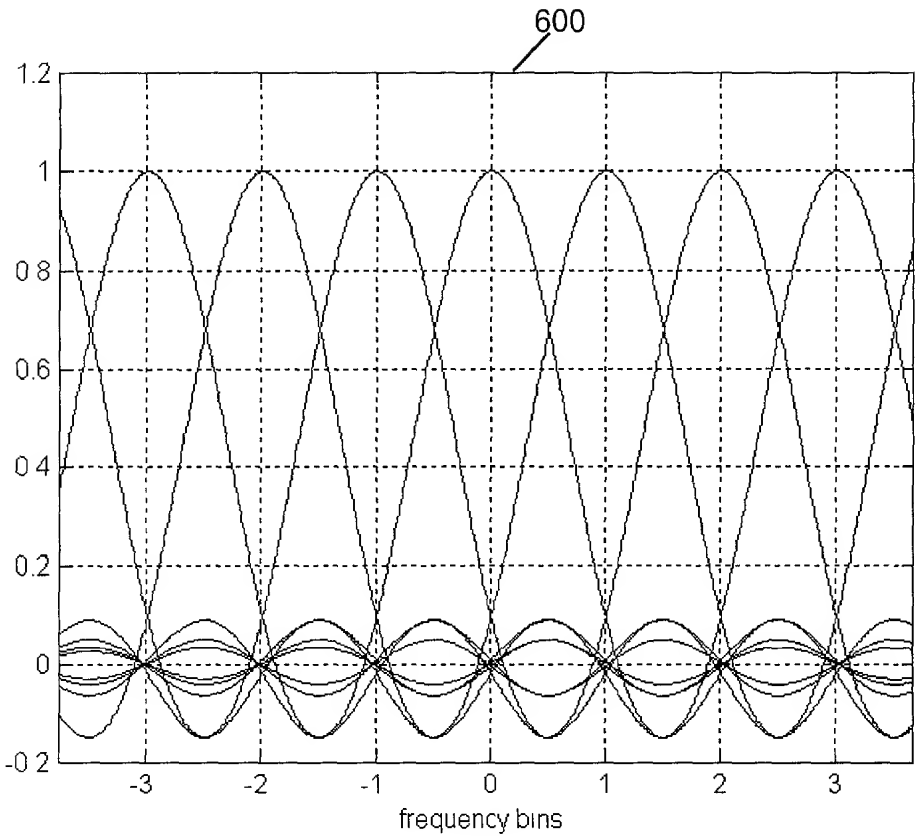

Figure 6

Patent Application Publication

TOF290 T6598860

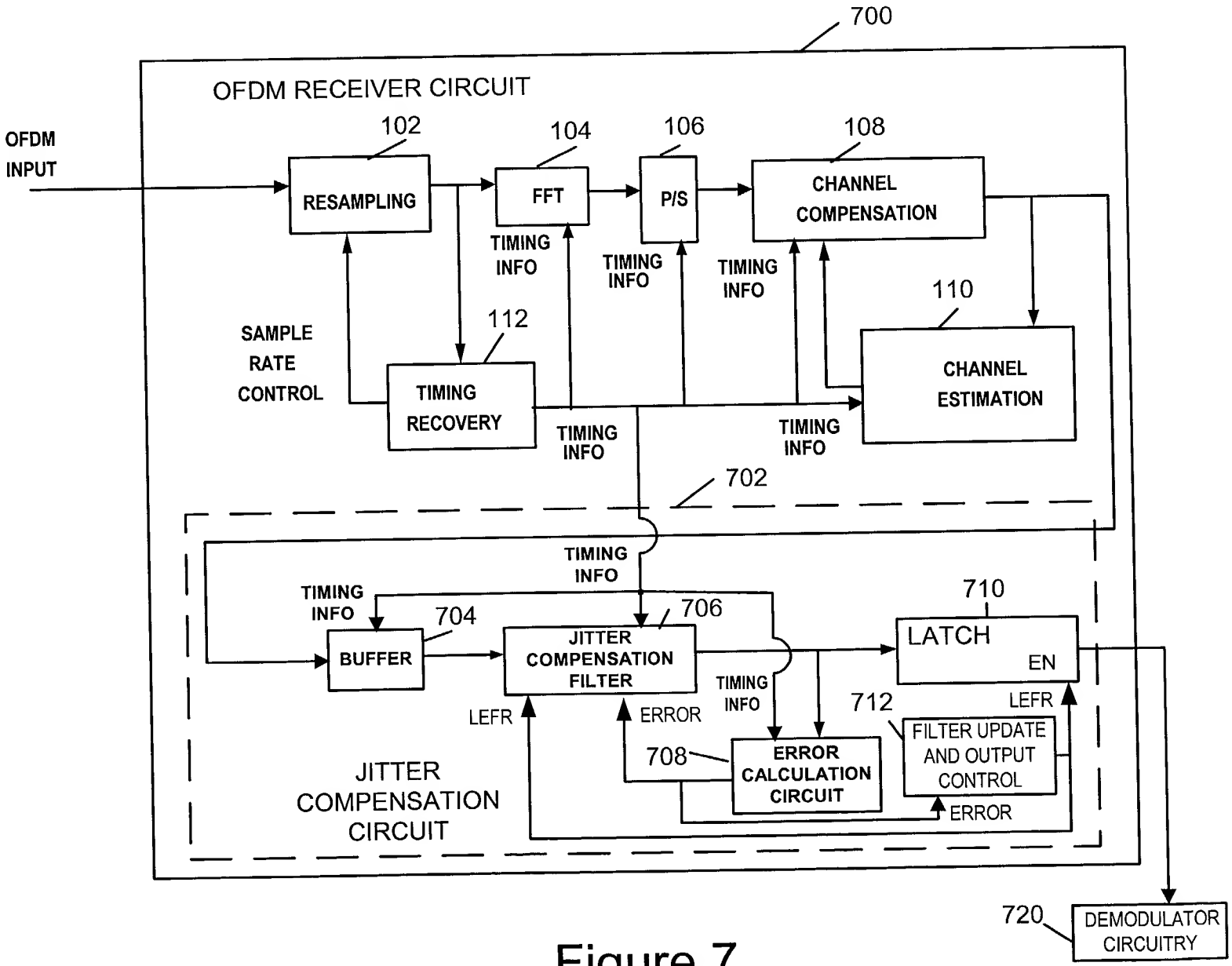

Figure 7

TOT290 T6598860

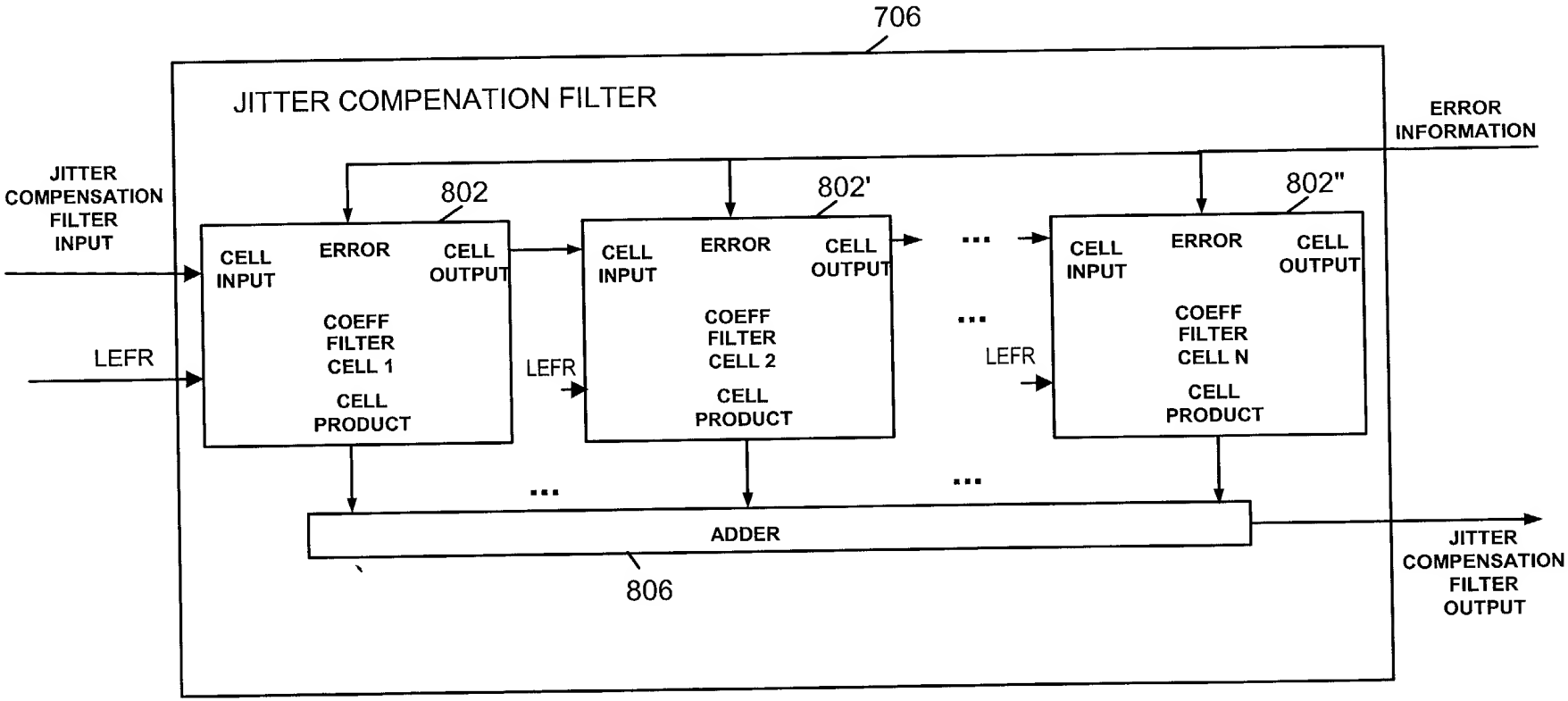

Figure 8

TDT290 T6598860

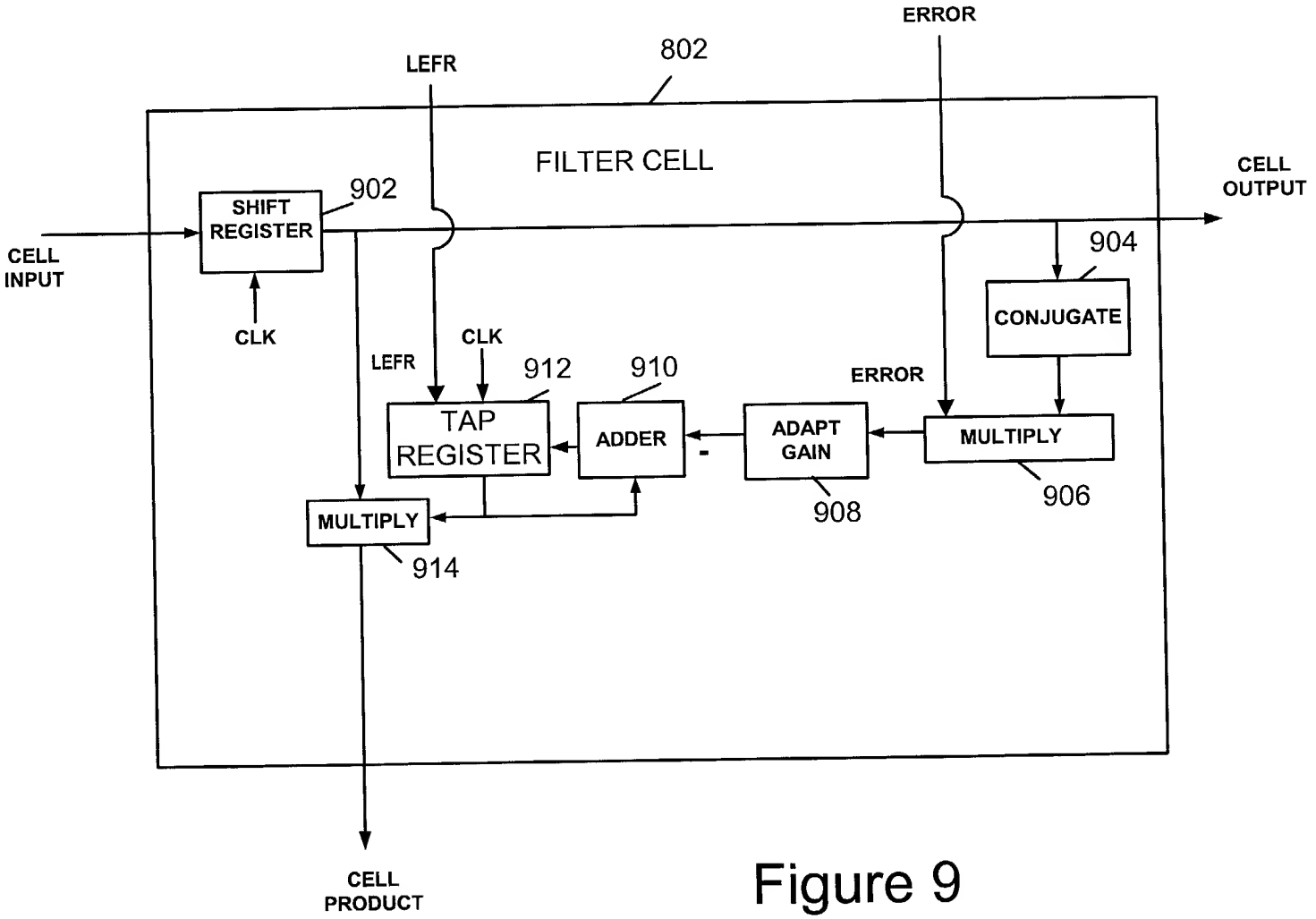

Figure 9

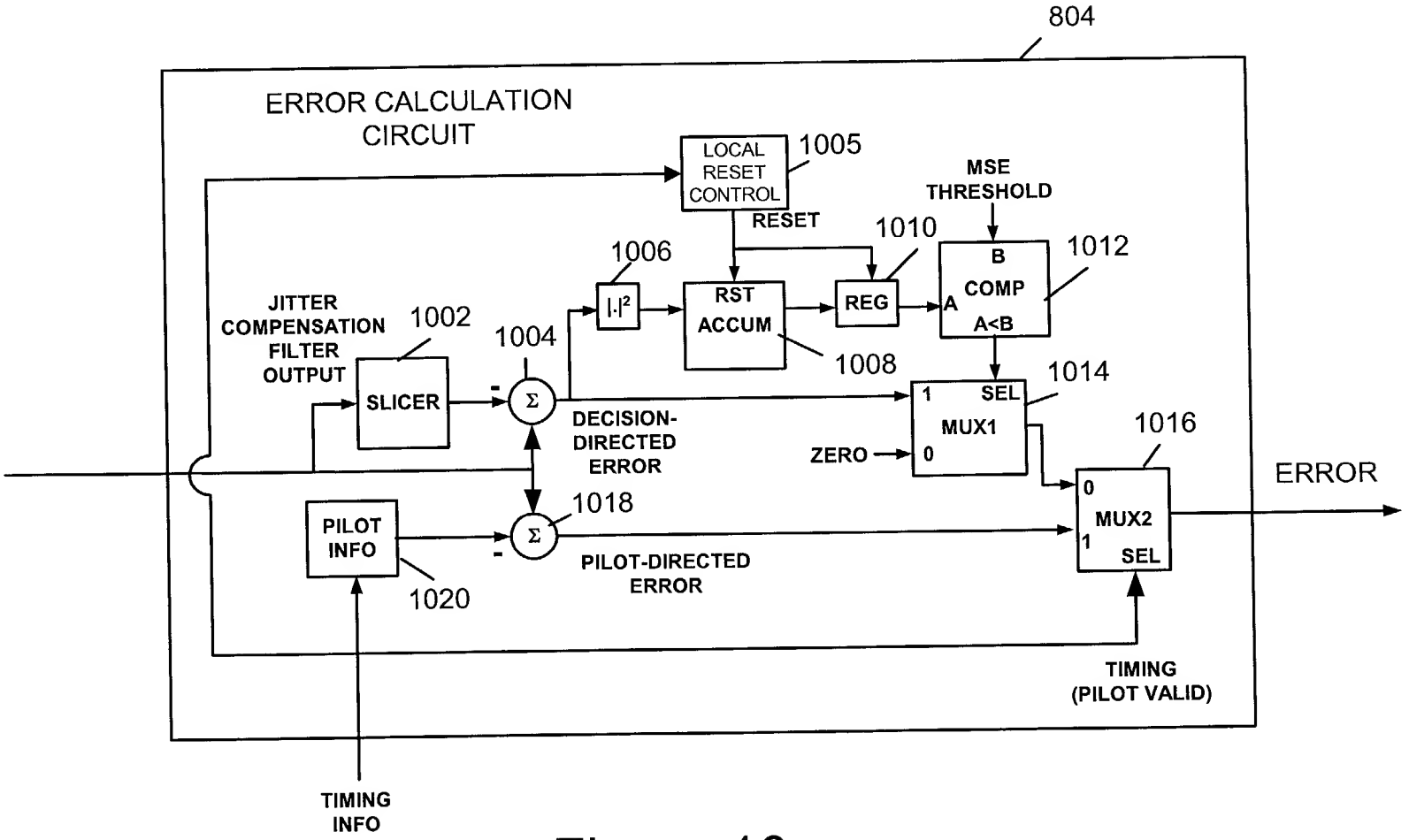
804
ERROR CALCULATION CIRCUIT
LOCAL RESET CONTROL
1005
RESET
MSE THRESHOLD
1010
1012
B
COMP
A
A<B
1006
|·|²
RST ACCUM
1008
REG
JITTER COMPENSATION FILTER OUTPUT
1002
SLICER
1004
Σ
DECISION-DIRECTED ERROR
1014
1
SEL
MUX1
0
ZERO
1016
ERROR
0
MUX2
1
SEL
PILOT INFO
1018
Σ
PILOT-DIRECTED ERROR
1020
TIMING (PILOT VALID)
TIMING INFO

Figure 10

TOT290 T6598860

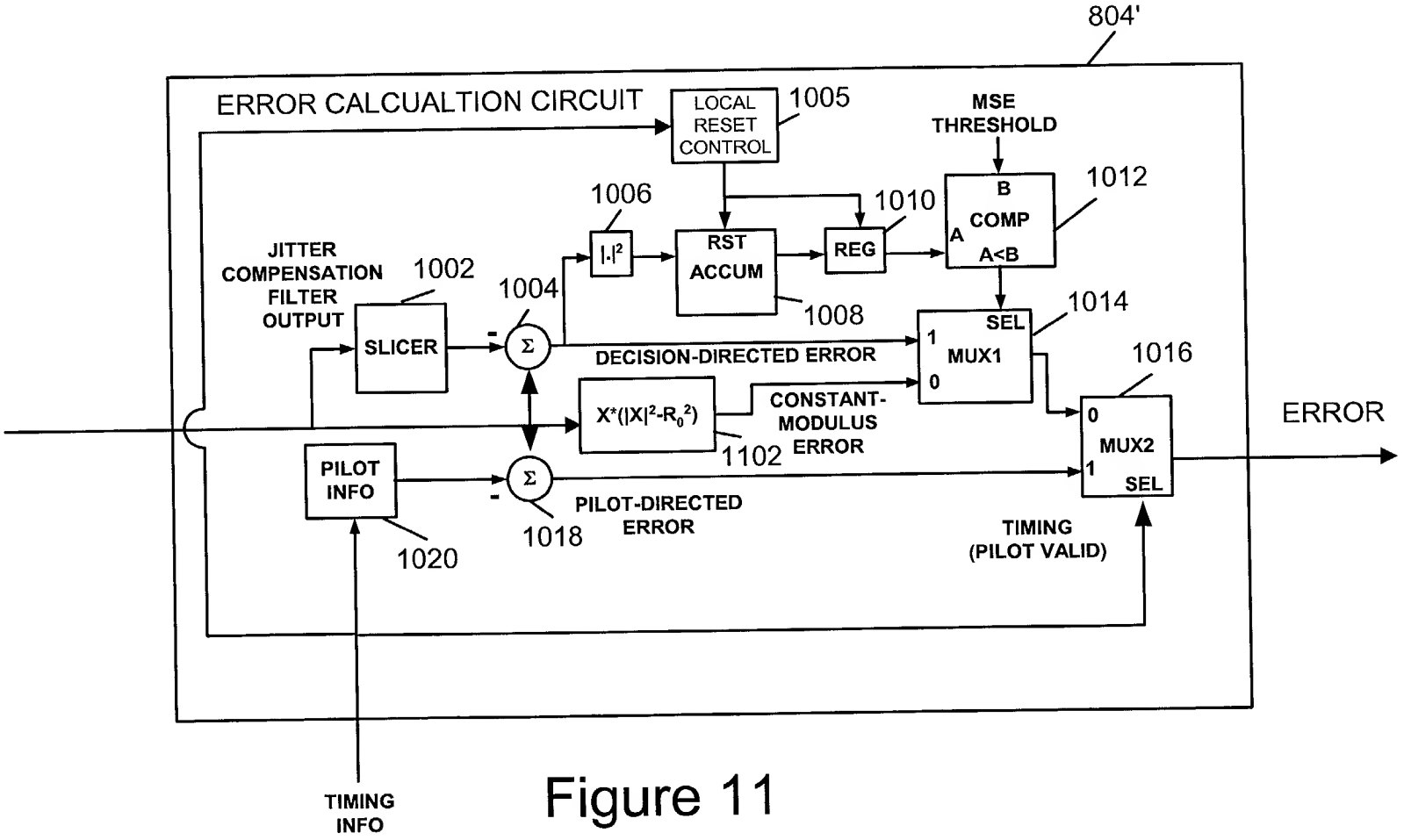

Figure 11

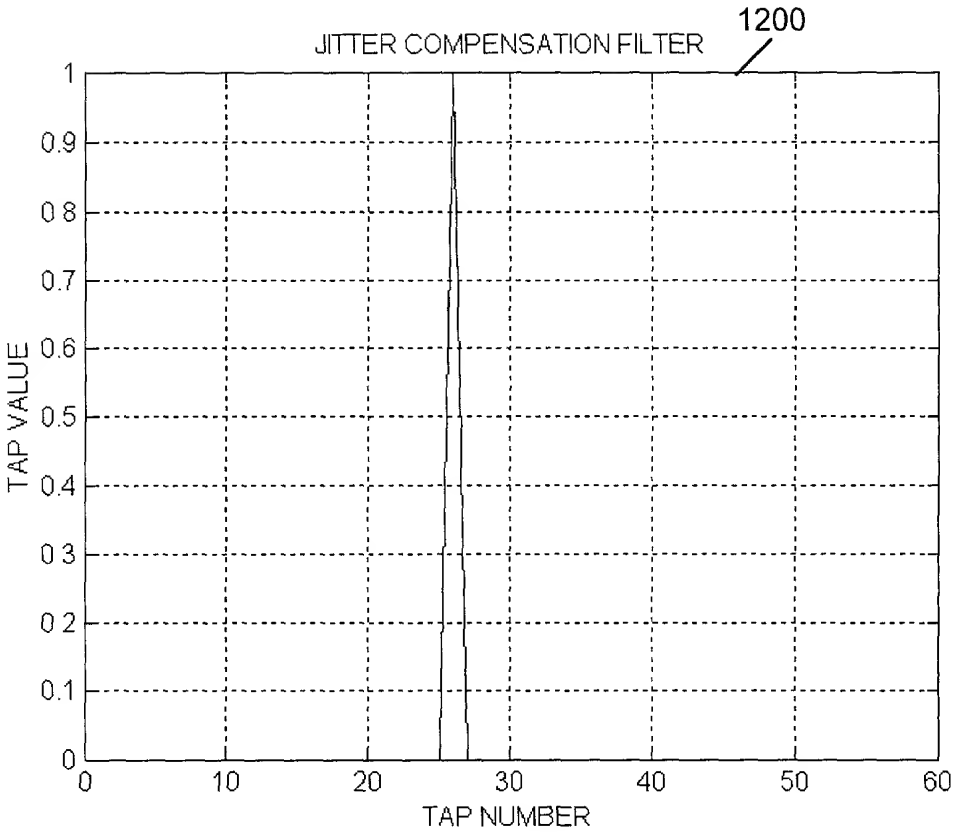
JITTER COMPENSATION FILTER
1200
TAP VALUE
TAP NUMBER
0
0.1
0.2
0.3
0.4
0.5
0.6
0.7
0.8
0.9
1
0
10
20
30
40
50
60

Figure 12

EP 2 276 204 A1

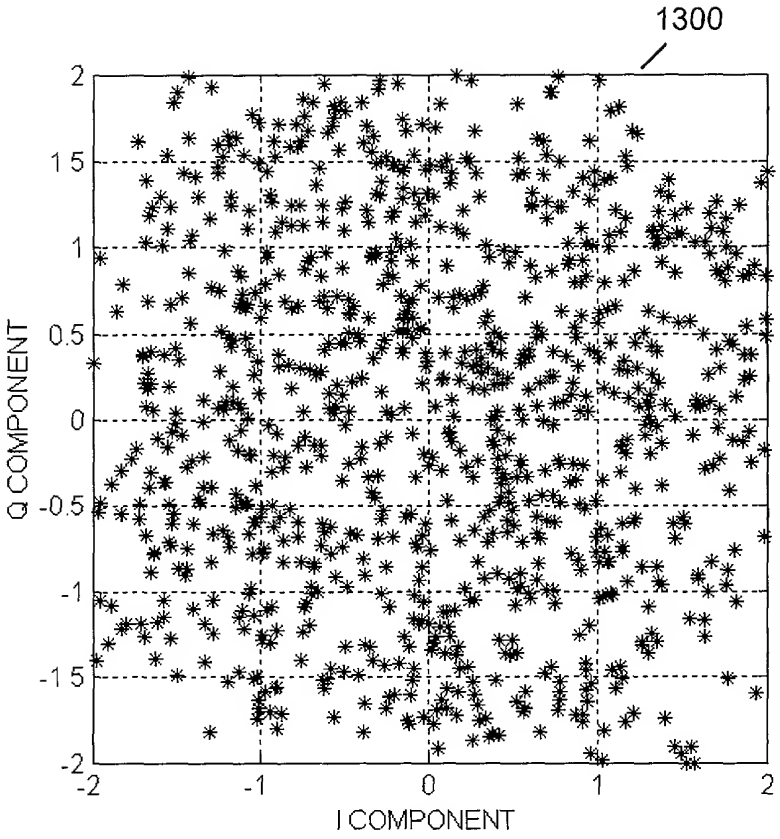

Figure 13

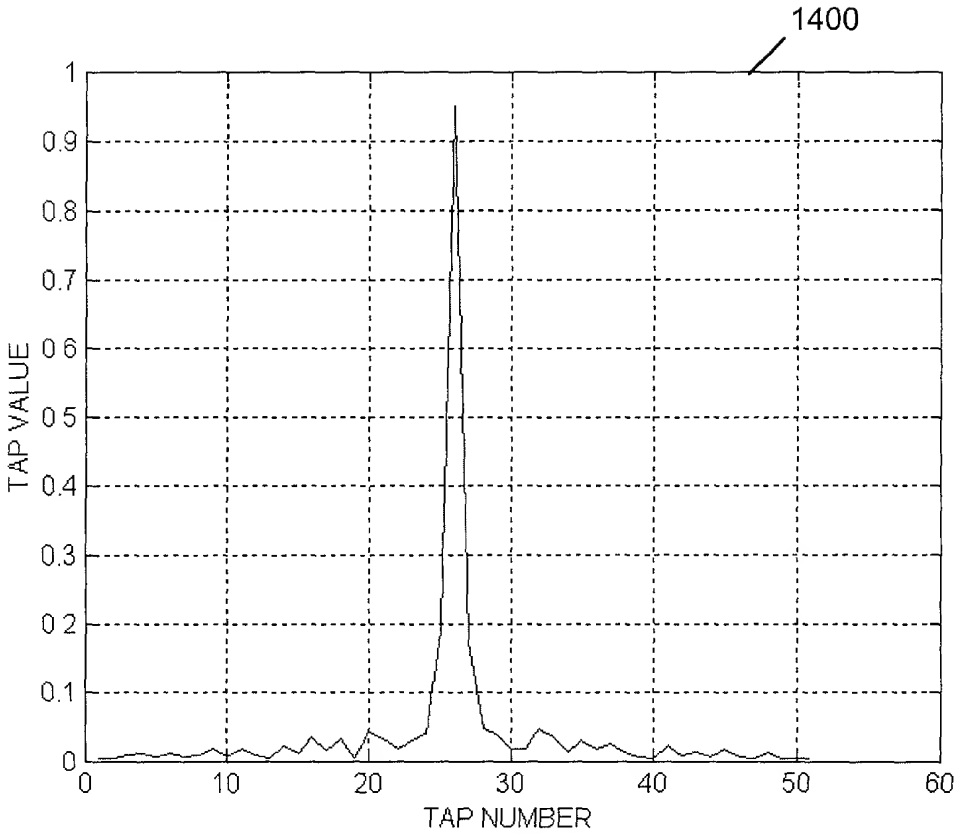

Figure 14

09865594.062504

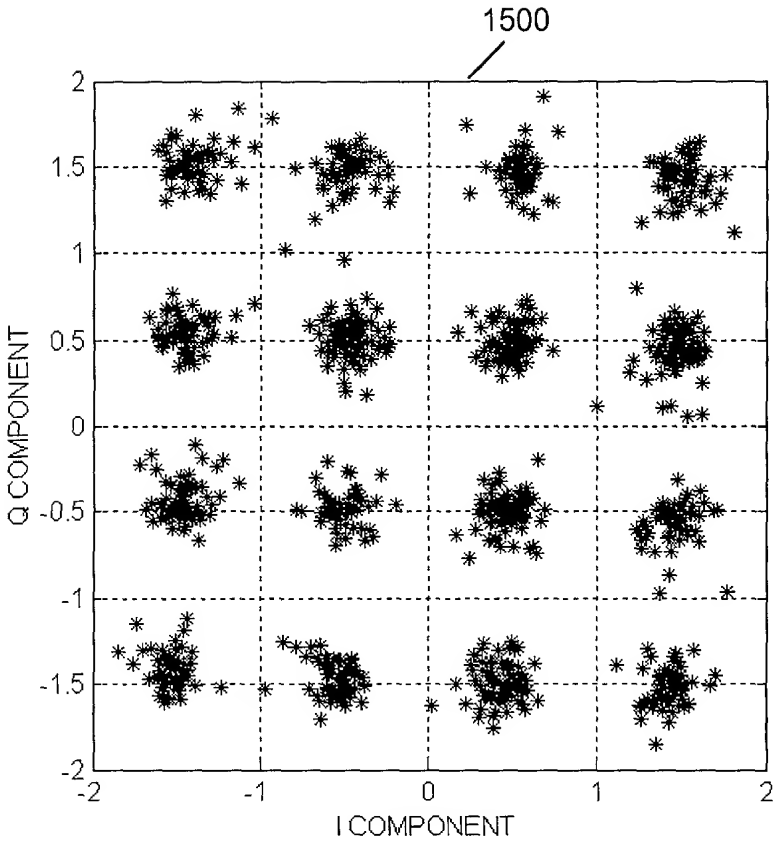

Fig. 15